

MODEL ANSWER

WINTER-18 EXAMINATION

Subject Title: Industrial Automation

Subject Code: 17664

Important Instructions to examiners:

- 1) The answers should be examined by key words and not as word-to-word as given in the model answer scheme.
- 2) The model answer and the answer written by candidate may vary but the examiner may try to assess the understanding level of the candidate.
- 3) The language errors such as grammatical, spelling errors should not be given more Importance (Not applicable for subject English and Communication Skills.
- 4) While assessing figures, examiner may give credit for principal components indicated in the figure. The figures drawn by candidate and model answer may vary. The examiner may give credit for anyequivalent figure drawn.
- 5) Credits may be given step wise for numerical problems. In some cases, the assumed constant values may vary and there may be some difference in the candidate's answers and model answer.
- 6) In case of some questions credit may be given by judgement on part of examiner of relevant answer based on candidate's understanding.
- 7) For programming language papers, credit may be given to any other program based on equivalent concept.

Q. No.	Sub Q.N.	Answer	Marki ng Schem e
Q.1	(A)	Attempt any Three:	12- Total Marks
	a)	State the benefits of automation.(four points)	4 M
	Ans:	 Benefits of Automation: Increases productivity. Increases product quality. Increases flexibility and convertibility. Reduces manpower. Reduction of personal accident/ Increase Safety. Reduces cost of product. Better inventory control. Increases profit. Reduce Manual Intervention. Reduce plant Shutdown & breakdown Time. Reduce plant startup time. 	1M for any 4 point
	b)	Draw block diagram of AC input module of PLC and explain.	4M
	Ans:		2M



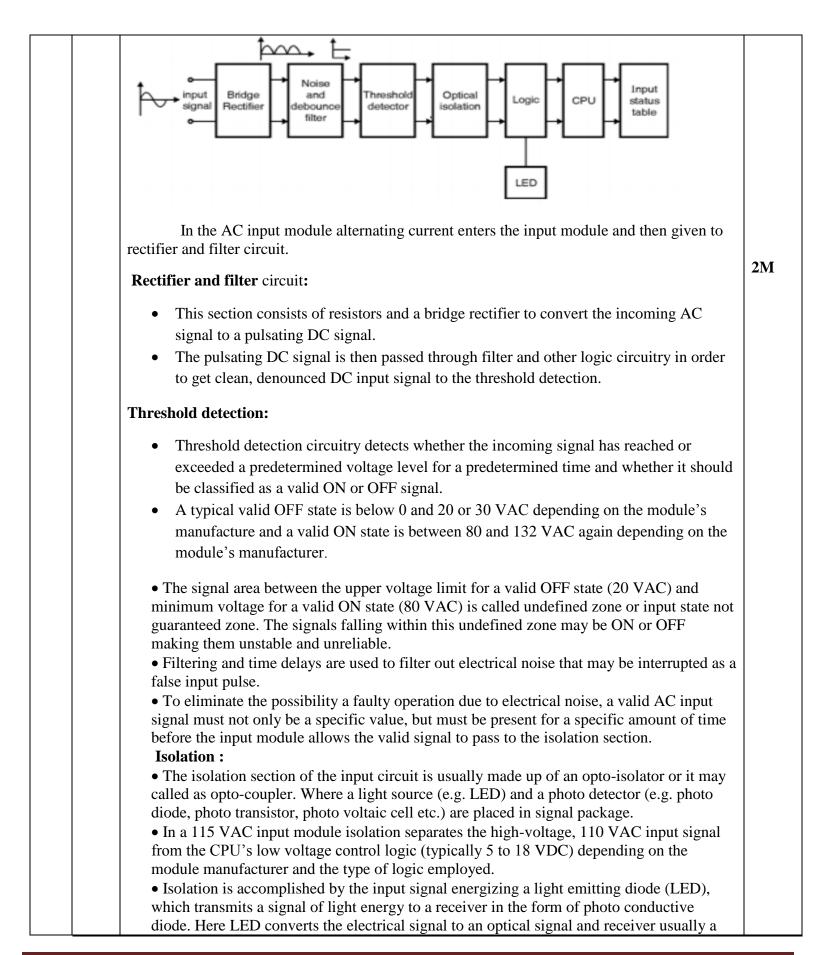


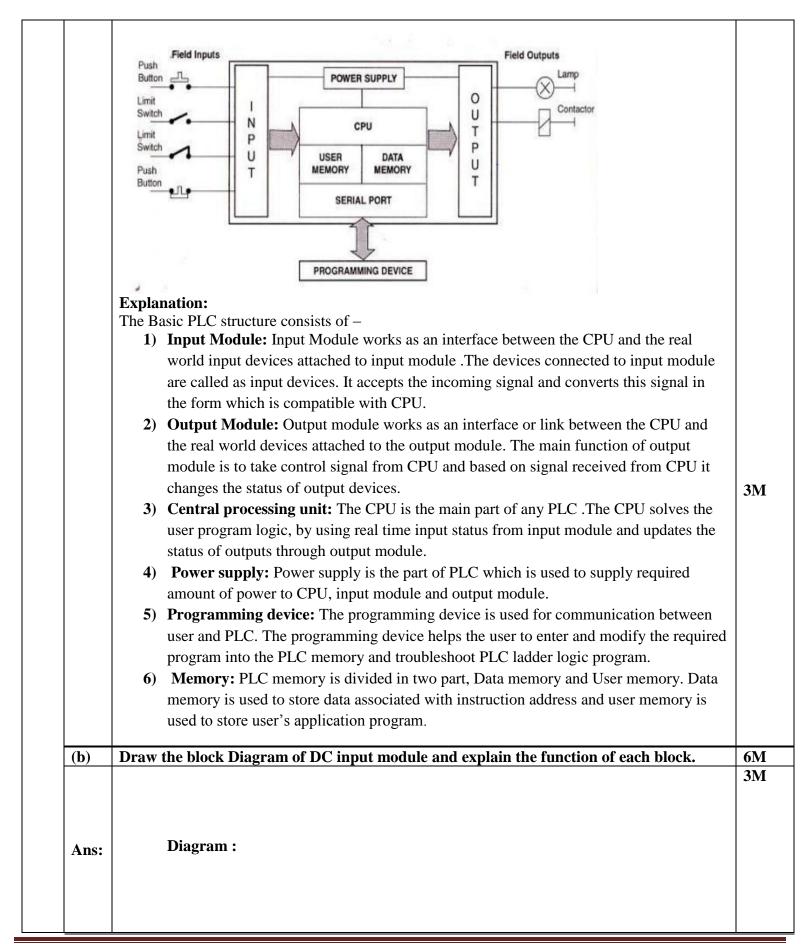


	 photo-transistor, converts the optical signal back to the electrical signal. There is no actual physical or electrical coupling between the sending LED, it's associated input circuitry and the optical receiver and it's low-voltage associated logic circuitry. The signal is transferred by light (photon particles) from the LED. The logic section : DC signals from the opto-coupler are used by the logic section to pass the input signal to the module's input address LED and the CPU. 	
c)	List relay type instruction of PLC with symbols.	4 M
Ans:	The relay type instructions are listed as , 1) NO B3:0 0 2) NC B3:0 0 3) One shot B3:0 0 $B3:0$ 0 0	1M for each (any four)
	0 4) Latch $ \int (f) \cdot f = 0 \text{(i)} \text{(j)} \text{(j)} $	



d)	List any four I/O module selection criteria.	4M
	Following points must be considered while selecting I/O modules:	
	 System Requirement. Application requirement. What in part (and part is no particular in a particular in the particula	1M for
	 What input /output capacity is required. What types of input/output are required? 	each (any
Ans:	5. Electrical requirements.	four)
	6. Speed of operation.	
	 Communication Requirements Software Requirements. 	
	 9. Operator interface requirements. 	
	10. Physical environment.	
(B)	Attempt any ONE:	6 M
(a)	Draw the block Diagram of PLC and give the function of each block.	6 M
Ans:	Diagram Dia	3М







		Improve the end of an optical isolator which separate high voltage from CPUsColspan="2">OO <th>3М</th>	3М
			11
Q 2		Attempt any TWO:	16- Total Marks
Q 2	a)	Attempt any TWO: (i)State types of PLC programming languages and explain. (ii) Give the types of miscellaneous instruction of PLC and explain. (i)State types of PLC programming languages and explain.	Total



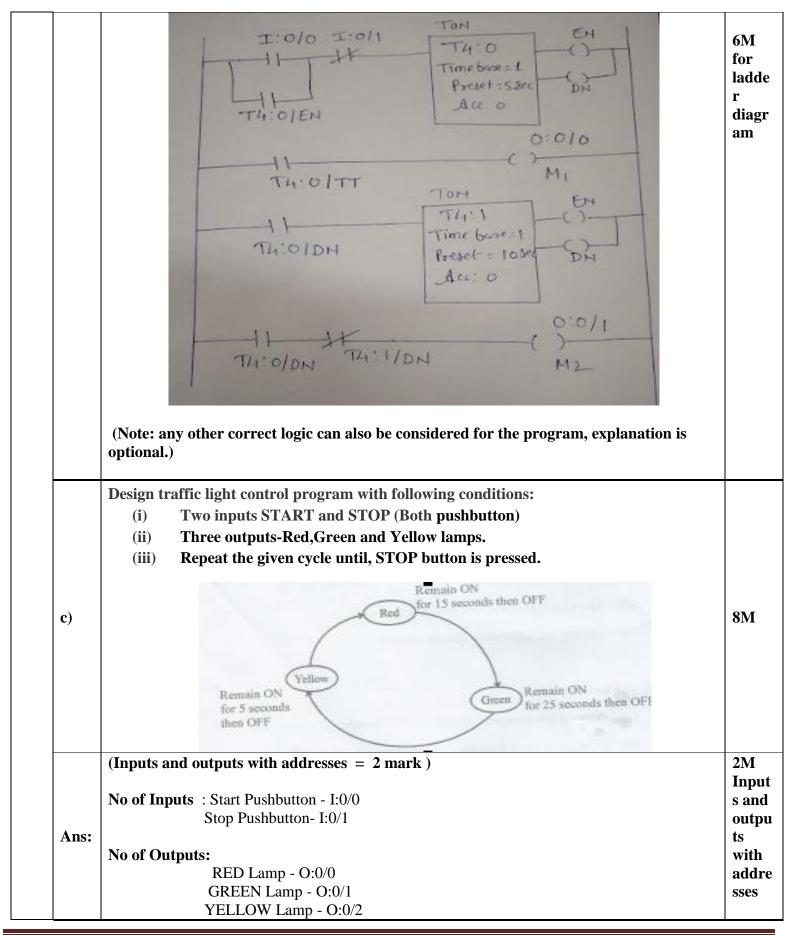
(ii)	Structured Text (ST)	
Exnl	anation of PLC programming	languages:
i)		t is a type of graphical language having the instructions
,		Ladder program is very similar to electrical wiring
	diagram, so it is easy to under	rstand.
ii)	Function Block Diagram (Fl	BD): The primary concept behind FBD is data flow in
	this instructions are compose	ed of operational blocks, Each block has one or more
	inputs and outputs.	
iii)	Sequential Function Chart o	r Grafcet (SFC): This language is used for performing
	simultaneously operations req	uired for controller in complex machine process
iv)	Instruction List (IL): It is si	imilar to assembly language programming, in this low
	level computer language like	e mnemonic codes are used to specify the operation of
	each rung of ladder diagram.	
	Characteria d Tarra (CT). It is a	high lavel commuter type longuage like Desig or C. It is
v)		high level computer type language like Basic or C. It is
v)		night level computer type language like Basic of C. It is ns on values other than binary.
,	capable to perform calculation	ns on values other than binary.
,	capable to perform calculation	
(ii) G	capable to perform calculation	instruction of PLC and explain.
(ii) G Types	capable to perform calculation	instruction of PLC and explain.
(ii) G Types 1.seque 2. seque	capable to perform calculation Give the types of miscellaneous of miscellaneous (sequencer) i encer output lencer compare	instruction of PLC and explain.
(ii) G Types 1.seque 2. seque 3. Seque	capable to perform calculation Sive the types of miscellaneous of miscellaneous (sequencer) i encer output lencer compare uencer load.	instruction of PLC and explain.
(ii) G Types 1.seque 2. seque 3. Seque 4. Scal	capable to perform calculation Give the types of miscellaneous of miscellaneous (sequencer) i encer output hencer compare hencer load. e with parameter	instruction of PLC and explain.
(ii) G Types 1.seque 2. seque 3. Seque 4. Scal 5. Subr	capable to perform calculation Give the types of miscellaneous of miscellaneous (sequencer) i encer output hencer compare hencer load. e with parameter	instruction of PLC and explain.
(ii) G Types 1.seque 2. seque 3. Seque 4. Scal	capable to perform calculation Give the types of miscellaneous of miscellaneous (sequencer) i encer output hencer compare hencer load. e with parameter	instruction of PLC and explain.
 (ii) G Types 1.seque 2. seque 3. Seque 4. Scal 5. Substitution 	capable to perform calculation Give the types of miscellaneous of miscellaneous (sequencer) i encer output hencer compare hencer load. e with parameter	instruction of PLC and explain.
 (ii) G Types 1.seque 2. seque 3. Seque 4. Scal 5. Substitution 	capable to perform calculation Give the types of miscellaneous of miscellaneous (sequencer) i encer output hencer compare hencer load. e with parameter routine	instruction of PLC and explain.
(ii) G Types 1.seque 2. seque 3. Seque 4. Scal 5. Subr 6. PID	capable to perform calculation Give the types of miscellaneous of miscellaneous (sequencer) i encer output hencer compare hencer load. e with parameter routine	Instruction of PLC and explain.
(ii) G Types 1.seque 2. seque 3. Seque 4. Scal 5. Subr 6. PID	capable to perform calculation Sive the types of miscellaneous of miscellaneous (sequencer) i encer output encer compare uencer load. e with parameter routine	Instruction of PLC and explain. Instruction: ENCER INSTRUCTIONS Functional Description Each 16-bit word represents 16 outputs
(ii) G Types 1.seque 2. seque 3. Seque 4. Scal 5. Subr 6. PID	capable to perform calculation Give the types of miscellaneous of miscellaneous (sequencer) if encer output tencer compare tencer load. e with parameter routine SECU routine SECU Control machine sequence	ENCER INSTRUCTIONS ENCER INSTRUCTIONS Encert Information Each 16-bit word represents 16 outputs on an output module. Outputs are controlled by sequencing from one word to the next
(ii) G Types 1.seque 2. seque 3. Seque 4. Scal 5. Subr 6. PID	capable to perform calculation Give the types of miscellaneous of miscellaneous (sequencer) if encer output tencer compare uencer load. e with parameter routine SEQU Other Use Use This Instruction to	ENCER INSTRUCTIONS

PLC sequencer replaces the mechanical drum sequence that was used to control the sequences of repeatable operations. It acts as pointer and points one of the word from block of data words stored in memory. It fetches the words one at a time from memory and transfer or move to another memory or to the output. When block of data is finished the PLC sequencer again point the first word from the block and process begins again. Traffic light controller is a simple example of sequencer which is controlled with electronics and PLC sequencer output.16 lights are used for output. Each light represent one bit address of output word 050.the lights are programmed in a four step sequence to simulate the operation of two way traffic light.Data are entered into word file for each sequencer step as shown fig.

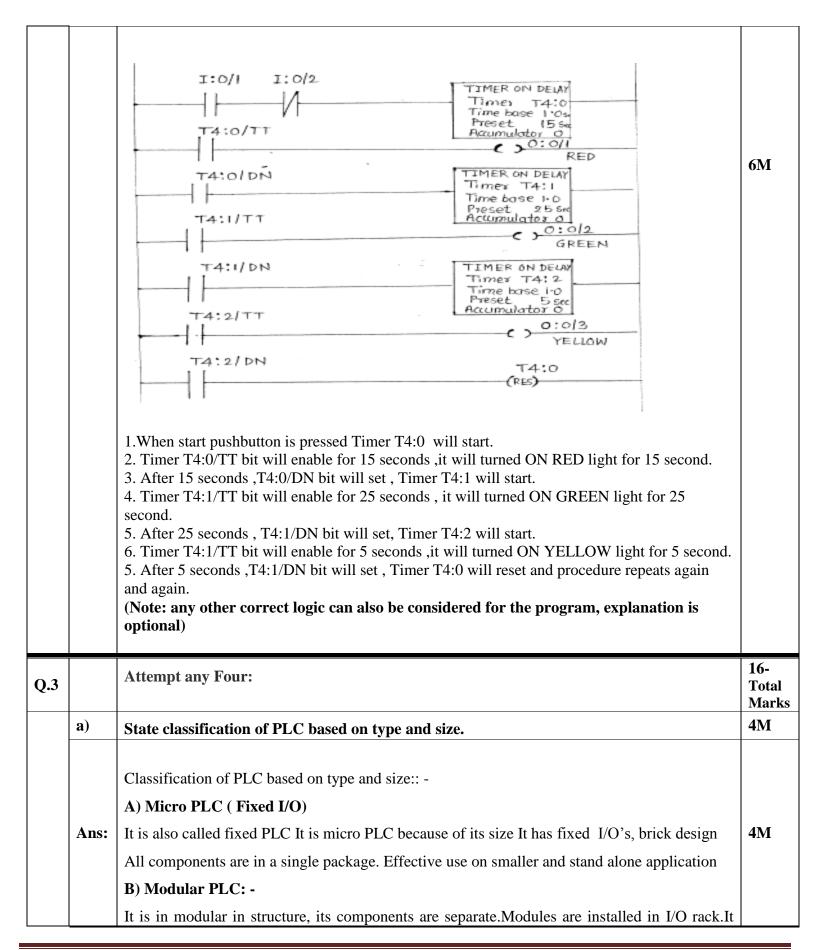


1		16	5 15	14	13	12	11	10	9	8	7	6	5	4	3	2	1		
	Output address Word 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	Word 0	0 0		0	0	1	0	0	0	0	0	0	0	0	0	0	1	Step 1	
	Four-word file Word (1	0 0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	Step 2	
	located in memory Word (2 1	0 0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	Step 3	
	Word (3	0 0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	Step 4	
	is transferred remains off. A a result lamp 1 step 4 is follow manually reset	nto lvar and ed a to s	wo ncir l 8 and tep	ord (lg th will fina 1.	050 ne s be ally) of sequ on ww	f o uer an her	utp nce nd a n la	out r to all t st s	as ste he tep	a re ep 2 rest is 1	esu 2 wi t wi read	lt 1 ill 1 ill 1 che	ligh tran be o ed, 1	nt 1 nsfe off the	l a er t an e se	nd he d t qu	information in word 060 of file 12 are switched ON and rest data from 061 to word 050, as hus advancement in step 3 and encer is either automatically or	
b)	Draw a ladded (i) Sta (ii) Aft (iii) Aft (iv) Sto pro	rt po er 5a er 10 o pu	ush sec Ose 1sh	but . mo c. n	tor oto 10to	n st r N or∶	tari 11 M2	ts r is (2 is	not OF O	or Fa FF.	M1 nd	mo	to	r M	[2]	is (DN	0	8M
Ans:	No of Inputs	Sto M	op I OT	Push Push OR (nbu 1 (N	ttoi	n-]) - ([:0/ O:(′1)/0	l									2M Input s and outpu ts with addre sses





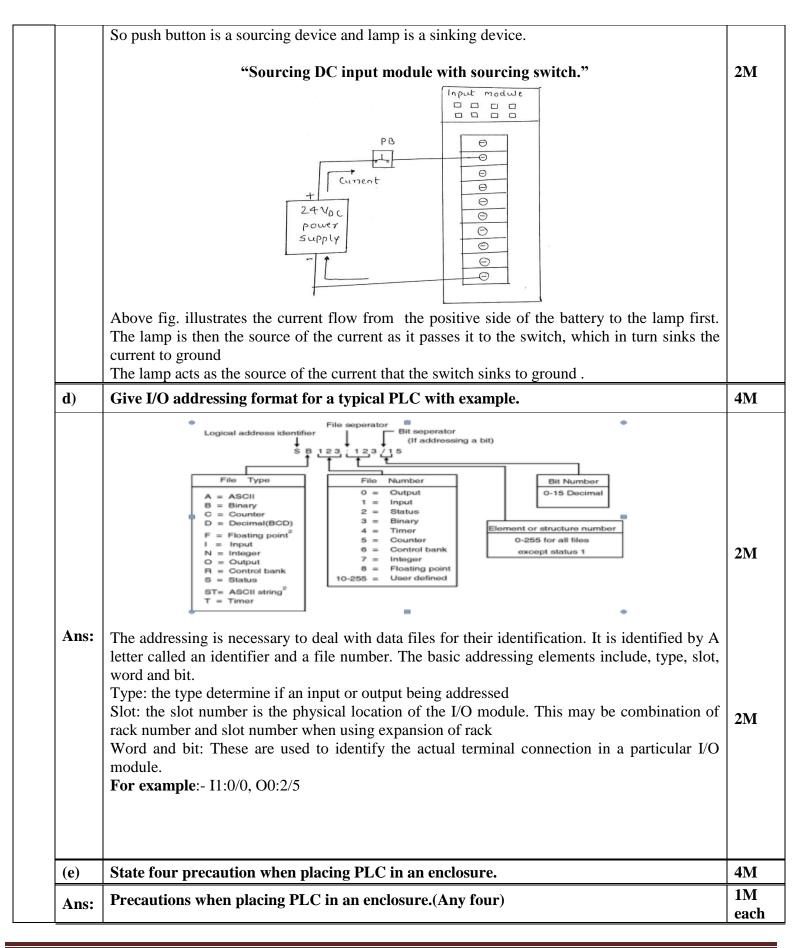






	 has following types: i) Small PLC : It has limited expansion capabilities. Having 20 inputs and 12 outputs are mounted on rack. Additionally less than 100 I/O's can be added through remote I/O rack. ii) Medium PLC: In this components are separate, also include mathematical function, file function etc. It can have 4000 to 8000 I/o' iii) Large PLC: It can hold multilplecards, can connected together as per requirement. It flexible and easy to maintain. It has I/O's more than medium PLC 	
b) Ans:	State the need of automation. Need of Automation in process : • To fulfill the demand of product at right time. • To reduce the human errors and involvement of human being in the process. • For better productivity. • For better control of process. • For better quality. • For reducing man power. • For reducing cost of product. OR Any other relevant points shall be considered	4M (1M each)
c) Ans:	Draw sinking type and sourcing type for DC input module. State the meaning of it.	4M 2M

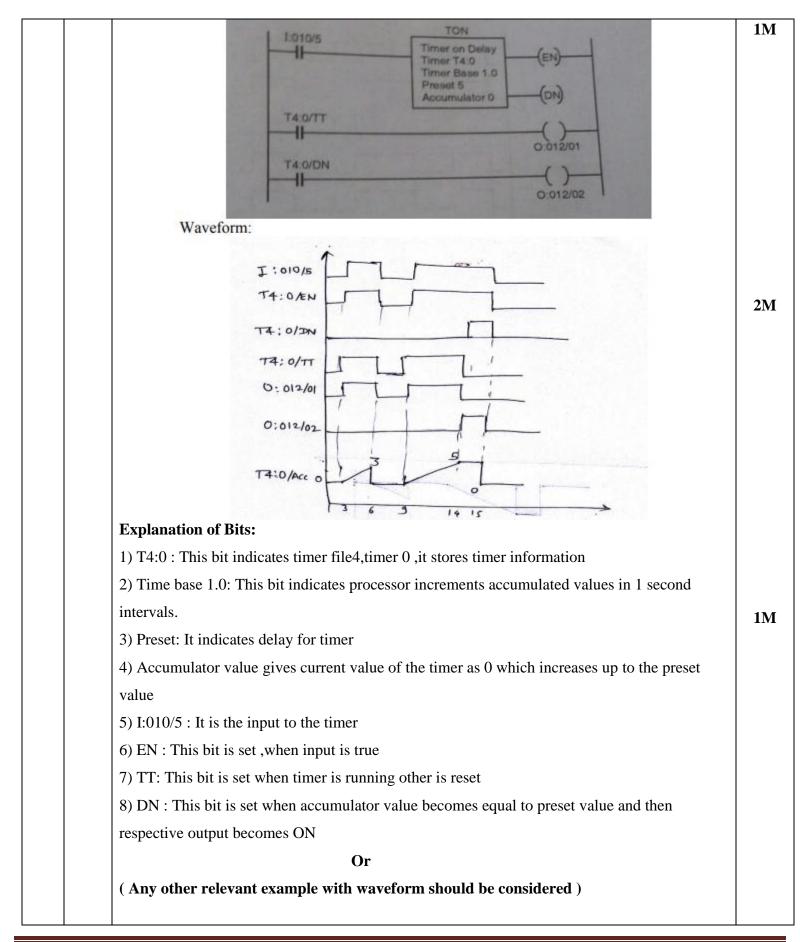






		1) Allow maximum convection cooling, all controller components should be mounted	
		vertically, in some cases components mounted horizontally will obstruct air flow	
		2)The power supply has a higher heat dissipation, so power supply should be	
		installed at the top of the enclosure above all other equipment, with adequate spacing	
		between power supply and the top of the enclosure	
		3) The CPU should be located at a comfortable working level that is either to or	
		below the power supply. If the CPU and power supply are contained in a single PLC	
		unit, then the PLC unit should be placed toward the top of the enclosure with no other	
		components directly above it, unless there is sufficient space	
		4) Do not install the control lines or communication cables together with the main	
		circuit lines or power cables. Keep a distance of 150mm or more between them.	
		Failure to do so may result in malfunction due to noise	
		5) At power-on or power-off, a voltage or current may flow between output terminals	
		momentarily. In this case, start the control after analog outputs become stable.	
		6)Periodically check the earthing of panel where PLC is enclosed	
		Note: Any other relevant precaution	
Q.4	(A)	Attempt any THREE:	12- Total Marks
Q.4	(A) a)	Attempt any THREE: Draw the format of ON delay timer and explain with timing waveforms.	
Q.4			Total Marks
Q.4		Draw the format of ON delay timer and explain with timing waveforms.	Total Marks
Q.4		Draw the format of ON delay timer and explain with timing waveforms.	Total Marks
Q.4		Draw the format of ON delay timer and explain with timing waveforms.	Total Marks
Q.4		Draw the format of ON delay timer and explain with timing waveforms.	Total Marks
Q.4		Draw the format of ON delay timer and explain with timing waveforms.	Total Marks
Q.4		Draw the format of ON delay timer and explain with timing waveforms.	Total Marks
Q.4	a)	Draw the format of ON delay timer and explain with timing waveforms.	Total Marks
Q.4	a)	Draw the format of ON delay timer and explain with timing waveforms.	Total Marks
Q.4	a)	Draw the format of ON delay timer and explain with timing waveforms.	Total Marks
Q.4	a)	Draw the format of ON delay timer and explain with timing waveforms.	Total Marks
Q.4	a)	Draw the format of ON delay timer and explain with timing waveforms.	Total Marks
Q.4	a)	Draw the format of ON delay timer and explain with timing waveforms.	Total Marks

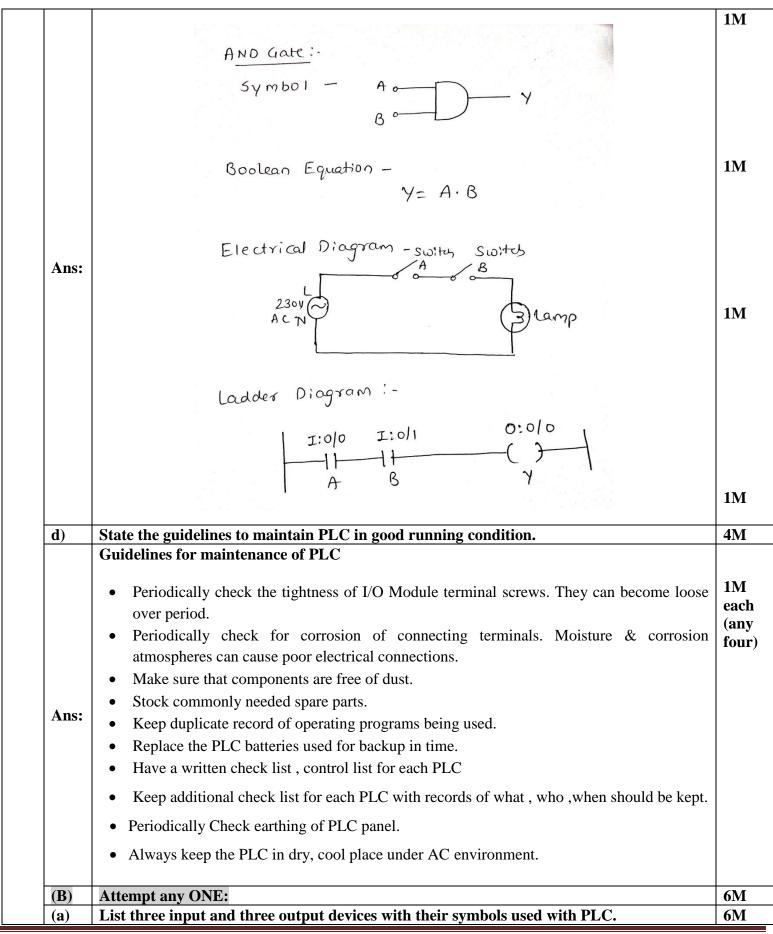




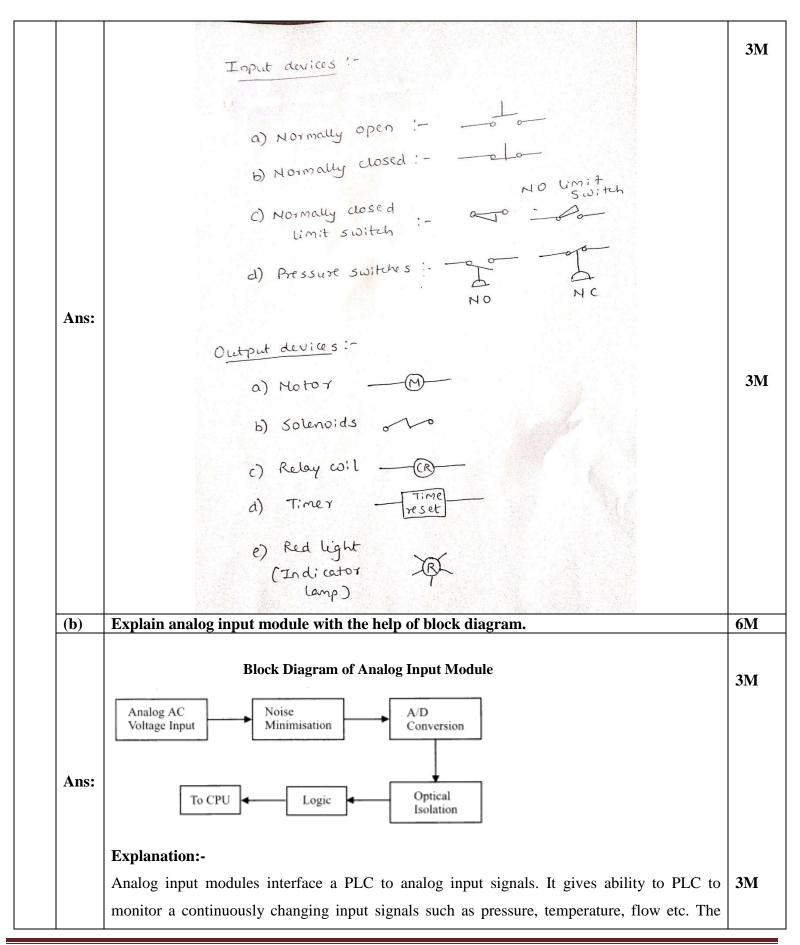


b)	State the concept of redundancy in PLC with a suitable diagram.	4 N
	Redundancy :	21
Ans:	 Redundancy means extra system components or mechanisms added to decrease the chance of total system failure. Different types of redundancy are available in PLC like redundancy for a CPU module, power module, bases and communication module is available. CPU redundancy system is composed of separate bases for idea redundancy structure. In case an error occurs in an active CPU module, a backup module is automatically converted to active one for continuous operation. In these cases two processors can be tied into one I/O system and some means is provided that switches control from the failure CPU to the backup when a failure occurs as shown in Fig. 	21
c)	Draw symbol, Boolean equation, electrical and ladder diagram for AND gate.	41





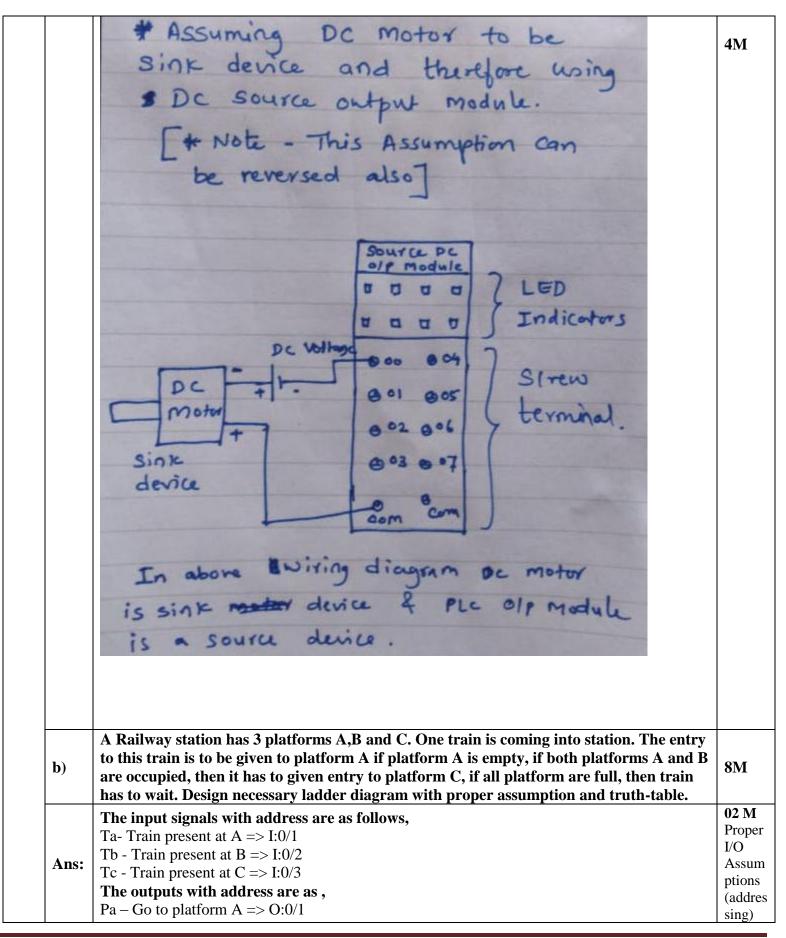






		module converts analog input signals to 16 bit binary values storage in the processor's input status table. Analog modules are designed to accept current and voltage signals such as 0-10 Vdc,-10-10 Vdc,0-5Vdc and 0-20mA,4-20mA,-20 -20mA etc. When signal reaches an input module, it is rich in different noise signals. The signal is freed from noise through noise Minimization circuits. The signal is then digitized and sent to logic section through an isolation circuit. The logic section allows the digitized signal to go to the CPU following the predetermined logic.	
Q.5		Attempt any Two:	16- Total Marks
	a)	(i) Draw a neat diagram of DC output module and give the function of each block.(ii) Draw wiring diagram to connect DC motor to PLC and specify type of output module can be used.	8M
	Ans:	(i) Draw a neat diagram of DC output module and give the function of each block Diagram:- (i) Block diagram of DC output module: (i) Block diagram of DC output module: (i) Block diagram of DC output module: (controll ed Device From CPU Latch Logic Circuit n	2M
		 DC output modules simply act as a switch to control output field device to control output field device. Each output point contains switching devices, which is located inside the output module it is seen that the module consists of some potential free contacts operated by signals from the CPU. Once this signal appears at the output terminals of the module, the indicating LED glows. This signal is again isolated from the output power circuit by an optoisolator, amplified by the amplifier and interfaced with the real world power circuit. Field output devices like coils, relays etc., powered by a power supply external to the PLC receive the signal to operate for final control action. (ii) Draw wiring diagram to connect DC motor to PLC and specify type of output module can be used. 	2M

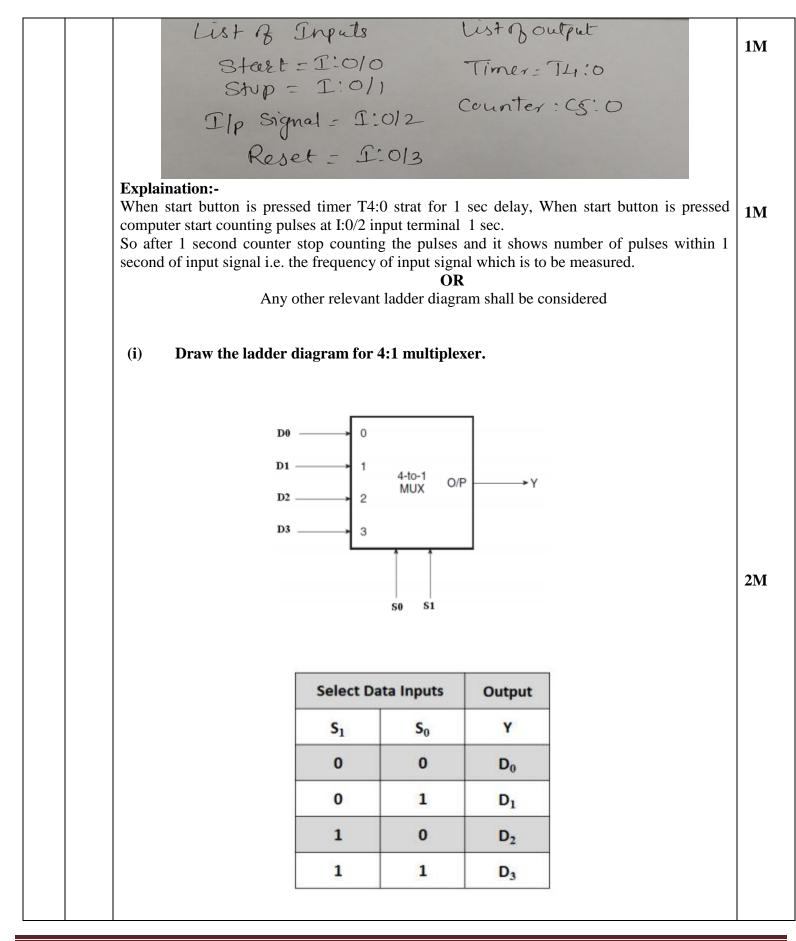






	Pb- Go to pla Pc - Go to p W -wait =>	latform C		}						02 marks for truth
		Input Ta	Input Tb	Input Tc	Output Pa	Output Pb	Output Pc	Output W]	table
		0	1	1	1	0	0	0		
		1	0	1	0	1	0	0		
		1	1	0	0	0	1	0		
		1	1	1	0	0	0	1		
	Any other re	elevant la	I I I dder dia	:0/1 1/ 0/1 1/ 0/1 1/ gram sha			O:0/1 () O:0/2 () O:0/3 () O:0/4 ()			04 marks for ladder diagra m
c)	(i) Write PLand explain(ii) Draw the	it.					vents usin	ig timer a	and counter	8M
Ans:	Diagram:-	6	T: 0/0 11 B3:0 B3:010 TH		OTI TA Das Poesi Acc	B3 AH CO CS CS CS	(CN) -(DA) ounter :0 - (C) -(D)			2M







		Expression: $Y = \overline{S1S0}D0 + \overline{S1}S0D1 + S1\overline{S0}D2 + S1S0D3$	
		Ladder Diagram 000 110 111 112 00 110 111 113 00 110 111 113 110 111 113 110 111 114 114 110 111 114 110 111 115 110 100 100 100 100 100 100 100 100 10	1M
		Explanation: For every combination of select lines that particular input is selected at output For eg. If S1=,S0=0; D0 will be selected at output ,Y=D0	1M
Q.6		Attempt any Two of the following ::	16- Total Marks
	a)	Give the types of specialty I/O module and explain any two.	4M
		 Different types of speciality I/O modules: 1) Communication module 2) RTD input module 3) High speed encoder 4) Stepper motor control 5) Thermocouple input module 6) Remote I/O sub scanner 1) Communication module: - The communication modules are used to communicate with 	1M for type
	Ans:	 1) Communication module: - The communication modules are used to communicate with programming devices, displays, plant computers, other PLC's. The four common communication modules are ASCII modules, local I/O adapter modules, the serial data modules, network interface modules. 2) RTD input module: - This module interfaces RTD's to a PLC and other types of resistance input devices such as potentiometers. It consists of bridge circuit filter, amplifier, and isolator circuits. 3) High speed encoder:-When input pulses come in faster than a discrete input module can handle them, a high speed input module is used. High speed counters are also used to interface encoder to a PLC. 4) Stepper motor control:-A stepper motor module is a intelligent module that resides in a PLC chassis and provides a digital output pulse train for microstepping stepper motor 	1 ½M Each (Any two)



	I/O chassis points. Aft	canner:- A subscanner scans ter the subscanner has scanner			
b)	stored in a build in build write the formats of	Up counter and explain w	ith waveforms.		41
Ans:	Diagram:	ts Ladder log Rung 1 1/0 CTU PB1 (Count) PB1 (Count) I 1/0 CS: 1/DN Rung 2 Counter Done Bit	NT-UP COUNTER (CU)	P Outputs L2 Red PL EXE OF Green PL	21



		Explanation: The counter is an output instruction whose function is to increment its accumulated value on false-to-true transitions of its instruction. It thus can be used to count false-to-true transitions of an input instruction and then trigger an event after a required number of counts or transitions. The up-counter output instruction will increment by 1 each time the counted event occurs. The program and timing diagram for a simple up-counter is shown in the figure. This control application is designed to turn the red pilot light on and the green pilot light off after an accumulated count of 7. Operating pushbutton PB1 provides the off-to-on transitions pulses that are counted by the counter. After 7 pulses, or counts, when the preset counter value equals the accumulated counter value, output DN is energized. As a result, rung to becomes true and energizes output O:2/0 to switch the red pilot light on. At the same time, rung 3 becomes false and de-energizes output O:2/1 to switch the green pilot light off. The counter is reset by closing pushbutton PB2, which makes rung 4 true and reset the accumulated count to zero. Counting can resume when rung 4 goes false again.	2M
c)State the necessity of grounding for PLC during installation.4NGrounding is necessary for PLC during installation:4N	c)	State the necessity of grounding for PLC during installation. Grounding is necessary for PLC during installation:	



d)	Any other relevant answer should be considered List four logical instructions and also draw their formats.	4 M
	List of logical instructions:	•10
	1. AND instruction	
	2. OR instruction	
	3. NOT instruction	
	4. X-OR instruction	
	5. N-AND instruction	
	6. N-OR instruction	
	7. X-NOR instruction	
	7. A-NOR Instruction	
	1. AND instruction : It performs the logical AND operation between two operands.	
	AND	
	Source A N7 : 0	
	Source B N7 : 1	
	Destination N7 : 2	
	THOU HAR OF THE IN STUDIED OUT D	
	2. OR instruction: It performs the logical OR operation between two operands. If one of	the
	bit or both the bits of two operands are 1 then output bit is 1 otherwise 0.	1N
		ea
Ange	OR	
Ans:	OR Source A N7 : 0	an
Ans:	Source A N7 : 0	an
Ans:	Source A N7 : 0 Source B N7 : 1	an
Ans:	Source A N7 : 0	an
Ans:	Source A N7 : 0 Source B N7 : 1	an
Ans:	Source A N7 : 0 Source B N7 : 1	an
Ans:	Source A N7 : 0 Source B N7 : 1	an
	Source A N7 : 0 Source B N7 : 1 Destination N7 : 2	an foi
	3. NOT instruction: It has single source and perform logical NOT operation and store re	an foi
	3. NOT instruction: It has single source and perform logical NOT operation and store rein destination memory.	an foi
	3. NOT instruction: It has single source and perform logical NOT operation and store rein destination memory. Output is complement of input.	an foi
	3. NOT instruction: It has single source and perform logical NOT operation and store rein destination memory.	an foi
	Source A N7 : 0 Source B N7 : 1 Destination N7 : 2 3. NOT instruction: It has single source and perform logical NOT operation and store rein destination memory. Output is complement of input. NOT instruction reverses all of the bits in the source word.	an foi
	Source A N7 : 0 Source B N7 : 1 Destination N7 : 2 3. NOT instruction: It has single source and perform logical NOT operation and store rein destination memory. Output is complement of input. NOT instruction reverses all of the bits in the source word.	an foi
	Source A N7 : 0 Source B N7 : 1 Destination N7 : 2 3. NOT instruction: It has single source and perform logical NOT operation and store rein destination memory. Output is complement of input. NOT instruction reverses all of the bits in the source word.	an foi
	Source A N7 : 0 Source B N7 : 1 Destination N7 : 2 3. NOT instruction: It has single source and perform logical NOT operation and store rein destination memory. Output is complement of input. NOT instruction reverses all of the bits in the source word.	an foi
	Source A N7 : 0 Source B N7 : 1 Destination N7 : 2 3. NOT instruction: It has single source and perform logical NOT operation and store rein destination memory. Output is complement of input. NOT instruction reverses all of the bits in the source word.	an foi
	Source A N7 : 0 Source B N7 : 1 Destination N7 : 2 3. NOT instruction: It has single source and perform logical NOT operation and store rein destination memory. Output is complement of input. NOT instruction reverses all of the bits in the source word.	anj fot
	Source A N7 : 0 Source B N7 : 1 Destination N7 : 2 3. NOT instruction: It has single source and perform logical NOT operation and store rein destination memory. Output is complement of input. NOT instruction reverses all of the bits in the source word.	esult
	Source A N7 : 0 Source B N7 : 1 Destination N7 : 2 3. NOT instruction: It has single source and perform logical NOT operation and store rein destination memory. Output is complement of input. NOT instruction reverses all of the bits in the source word.	esult



	XOR Source A N7 : 0 Source B N7 : 1 Destination N7 : 2	
e)	Explain the procedure of troubleshooting of ladder program in PLC.	4 M
Ans:	 A hardware fault in the memory IC that holds the ladder logic program could alter the program, but this is a PLC hardware failure. If all other possible source of trouble have been eliminated, the ladder logic program should be reloaded into the PLC from the master copy of the program. Start program troubleshooting by identifying which outputs operate properly and which outputs do not. Then, using the programming software and search function, trace back from the output on the nonfunctioning rung and examine the logic to determine what may be preventing the output from energizing. Common logic errors include: Programming an examine-for-on instructions instead of an examine-for-off (or vice versa). Using an incorrect address in the program needs to be modified to include this new state. A careful examination of the description of the control system and the ladder logic program can help identify this type of fault. The suspend (SUS) instruction is used to trap and identify specific conditions for program debugging and system troubleshooting. When the rung is true, this instruction places the controller in the suspend idle mode .operation is suspended and the suspend ID number is placed in word7 (S:7) of the status file so that you can track ladder program. As a result, multiple rung conditions can control the same output coil, making troubleshooting more difficult. In the case of duplicate outputs the monitored rung may be true; but if a rung farther down in the ladder diagram is false, the PLC will keep the output off. Some software allows foe checking multiple coil use. 	4M