

(ISO/IEC - 27001 - 2005 Certified)

## WINTER – 2022 EXAMINATION MODEL ANSWER

### Subject: Operating System

**Subject Code:** 

22516

#### Important Instructions to examiners:

- 1) The answers should be examined by key words and not as word-to-word as given in the model answer scheme.
- 2) The model answer and the answer written by candidate may vary but the examiner may try to assess the understanding level of the candidate.
- 3) The language errors such as grammatical, spelling errors should not be given more Importance (Not applicable for subject English and Communication Skills.
- 4) While assessing figures, examiner may give credit for principal components indicated in the figure. The figures drawn by candidate and model answer may vary. The examiner may give credit for anyequivalent figure drawn.
- 5) Credits may be given step wise for numerical problems. In some cases, the assumed constant values may vary and there may be some difference in the candidate's answers and model answer.
- 6) In case of some questions credit may be given by judgement on part of examiner of relevant answer based on candidate's understanding.
- 7) For programming language papers, credit may be given to any other program based on equivalent concept.
- 8) As per the policy decision of Maharashtra State Government, teaching in English/Marathi and Bilingual (English + Marathi) medium is introduced at first year of AICTE diploma Programme from academic year 2021-2022. Hence if the students in first year (first and second semesters) write answers in Marathi or bilingual language (English +Marathi), the Examiner shall consider the same and assess the answer based on matching of concepts with model answer.

Q.	Sub		Marking					
No	Q.N.		Scheme					
1.		Attempt any F	IVE of the following:		10			
	<b>a</b> )	Differentiate be	Differentiate between Multi programmed and Multitasking operating					
		system (Any two	points)					
	Ans.	Features	Multiprogramming	Multitasking				
					Any two			
		Basic	It allows multiple	A supplementary of the	relevant			
			programs to utilize	multiprogramming	points, 1M each			
			the CPU	system also allows for	1111 Cuch			
			simultaneously.	user interaction.				
		Mechanis	Based on the context	Based on the time-				
		m	switching	sharing mechanism.				
			mechanism.					
		Objective	It is useful for	It is useful for running				
			reducing/decreasing	multiple processes at the				



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		CPU idle time and	same time, effectively	
		increasing	increasing CPU and	
		throughput as much	system throughput.	
		as possible.		
	Execution	When one job or	In a multiprocessing	
		process completes its	system, multiple	
		execution or switches	processes can operate	
		to an I/O task in a	simultaneously by	
		multi-programmed	allocating the CPU for a	
		system, the system	fixed amount of time.	
		momentarily		
		suspends that		
		process. It selects		
		another process from		
		the process		
		scheduling pool		
		(waiting queue) to		
	CDU	In a multiusar	In a single user	
	Switching	a multiuser environment the	a single-user environment the CPU	
	Switching	CPU switches	switches between the	
		between	processes of various	
		programs/processes	programs.	
		quickly.	r - 8	
	Timing	It takes maximum	It takes minimum time to	
	0	time to execute the	execute the process.	
		process.	-	
			~	
b)	List any four s	ervices provided by O.S	8.	2M
Ans.	• User Int	ertace		16 M agab
	Program	n Execution		for any 4
	I/O Ope	ration		services
	File system Manipulation			
	Commu	nication		
	Error De	etection		
	Resource	e Allocation		
	Account	ting		
	Protection	on and security		
		······································		



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	c) Ans	<b>Define : Process</b>	, PCB	ution Process is also ca	lled	2M
	Ans.	as job, task or un	it of work.		incu	Correct Definition
		<b>PCB:-</b> Process C information of the also known as a t	Control Block is a dat ne process related to it. T ask control block, entry o	ta structure that cont he process control bloc f the process table, etc.	ains k is	1M each
	d) Ans.	Define CPU and CPU burst cycle	I <b>I/O burst cycle.</b> It is a time period where	n process is busy with C	PU.	2M Correct
		<b>I/O burst cycle:</b> with I/O resource	It is a time period when es.	process is busy in worl	king	Definition 1M each
	e)	Differentiate be	tween paging and segme	ntation.		2M
	Ans.	Parameters	Paging	Segmentation		Any two relevant
		Individual Memory	In Paging, we break a process address space into blocks known as pages.	In the case Segmentation, we bre a process address spa into blocks known sections/segments.	of ak ice as	differences – 1M each
		Memory Size	The pages are blocks of fixed size.	The sections/segmen are blocks of varyi sizes.	nts ng	
		Accountability	The OS divides the available memory into individual pages.	The compiler main calculates the size individual segmen their actual address well as virtual address	nly of ts, as	
		Speed	This technique is comparatively much faster in accessing memory.	This technique comparatively mu slower in accessi memory than Paging.	is ch ng	
		Size	The available memory determines the individual page sizes.	The user determines t individual segme sizes.	he ent	
		Fragmentation	The Paging technique may underutilize some of the pages- thus	The Segmentati technique may not u some of the memo	on ise ory	
		Size Fragmentation	faster in accessing memory. The available memory determines the individual page sizes. The Paging technique may underutilize some of the pages- thus	slower in accessi memory than Paging. The user determines t individual segme sizes. The Segmentati technique may not u some of the memo	he ent on ise ory	



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			leading to internal fragmentation.	blocks at all. Thus, may lead to extern fragmentation.	it nal
		Logical Address	A logical address divides into page offset and page number in the case of Paging.	A logical addre divides into section offset and section number in the case Segmentation.	ess on on of
		Data Storage	In the case of Paging, the page table leads to the storage of the page data.	In the case Segmentation, the segmentation table lea to the storage of the segmentation data.	of he ds he
	f)	Write syntax of (i) Kill	following commands-		2M
	Ans.	(ii) Sleep i) kill Syntax: kill Pid			IM for each correct syntax
		ii) sleep Syntax: sleep NU sleep OPTION	MBER[SUFFIX]		
	g) Ans	List any four file operations.			2M
	7	<ul> <li>Creating a file</li> <li>Writing a file:</li> <li>Reading a file:</li> <li>Repositioning within a file</li> <li>Deleting a file</li> </ul>			
		<ul> <li>Appending new information to the end of the file</li> </ul>			
		<ul> <li>Renaming an existing file.</li> <li>Creating copy of a file, copy file to another I/O device such as printer or display</li> </ul>			
2.	a)	Attempt any <u>TH</u> Explain Time sh	IREE of the following:		12 4M
	Ans.	In time sharing s among them. The	ystem, the CPU executes as switches occur so fre	multiple jobs by switch quently that the users	ting can



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time, memory, files and I/O devices to complete the job execution. These resources can be given to the process when it is created or

allocated to it while it is running.



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The operating system respo	nsible for the following activitie	s in
connection with process mana	gement:	
Creation and deletion of us	ser and system processes.	
Suspension and resumptio	n of processes.	
A mechanism for process	synchronization.	
• A mechanism for process	communication.	
• A mechanism for deadlock	k handling.	
2. Main-Memory Manageme	ent	
Main memory is a large array	of words or bytes, ranging in size f	from
hundreds of thousands to bil	llions. Each word or byte has its	own
address. Main memory is a	repository of quickly accessible	data
shared by the CPU and I/O	devices. The central processor r	eads
instructions from main memory	ry during the instruction fetch cycle	and
both reads and writes data from	om main memory during the data f	fetch
cycle. The main memory is g	generally the only large storage de	evice
that the CPU is able to address	s and access directly.	
The operating system respon	nsible for the following activitie	s in
connection with main memory	s management:	
Keeping track of which pa	arts of memory are currently being	used
and by whom.		
Deciding which processes	(or parts thereof) and data to move	into
and out of memory. 3. All	ocating and deallocating memory s	pace
as needed.		
3. File Management		
A file is a collected of relat	ed information defined by its cre	ator.
Computer can store files on	the disk (secondary storage), w	hich
provide long term storage.	Some examples of storage media	are
magnetic tape, magnetic disk	and optical disk. Each of these m	ledia
has its own properties like spe	eed, capacity, and data transfer rate	and
access methods. A file system	i normally organized into directorie	es to
ease their use. These direct	ctories may contain files and o	other
directions.	nsible for the following estimities	a in
The operating system responses	nsible for the following activitie	<b>S</b> 111
The greation and delation	ent:	
• The creation and deletion	of dimentions	
• The creation and deletion	01 directions.	_
• The support of primitives	for manipulating files and directions	S.
• The mapping of files onto	secondary storage.	
• The backup of files on stal	ole storage media.	



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	<ul> <li>4. I/O device Management</li> <li>Input / Output device management provides an environment for the better interaction between system and the I / O devices (such as printers, scanners, tape drives etc.). To interact with I/O devices in an effective manner, the operating system uses some special programs known as device driver. The device drivers take the data that operating system has defined as a file and then translate them into streams of bits or a series of laser pulses (in regard with laser printer). The I/O subsystem consists of several components:</li> <li>A memory management component that includes buffering, caching, spooling</li> <li>A general device driver interface</li> <li>Drivers for specific hardware devices</li> <li>5. Secondary-Storage Management</li> <li>The computer system provides secondary storage to back up main memory. Secondary storage is required because main memory is too small to accommodate all data and programs, and the data that it holds is lost when power is lost. Most of the programs including compilers, assemblers, word processors, editors, and formatters are stored on a disk until loaded into memory. Secondary storage consists of tapes drives, disk drives, and other media.</li> <li>The operating system is responsible for the following activities in connection with disk management:</li> <li>Free space management</li> <li>Storage allocation</li> <li>Disk scheduling.</li> </ul>	
c)	Explain shared memory model of Interprocess communication	4M
Ans.	<b>Inter-process communication:</b> Cooperating processes require an Inter- process communication (IPC) mechanism that will allow them to exchange data and information.	Explanation 3M Diagram 1M



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	Shared memory Proce	ess P1 1 Region of 2 ess P2 2 rnel	
	<ul> <li>Shared Memory System</li> <li>In this, all processes who want to communicate with other processes can access a region of the memory residing in an address space of a process creating a shared memory segment.</li> <li>All the processes using the shared memory segment should attach to the address space of the shared memory. All the processes can exchange information by reading and/or writing data in shared memory segment.</li> <li>The form of data and location are determined by these processes who want to communicate with each other.</li> <li>These processes are not under the control of the operating system.</li> <li>The processes are also responsible for ensuring that they are not writing to the same location simultaneously.</li> <li>After establishing shared memory segment, all accesses to the shared memory segment are treated as routine memory access and</li> </ul>		
d) Ans.	<ul> <li>Describe different scheduling cr</li> <li>CPU utilization: - In multipic keep CPU as busy as possible to 100 percent.</li> <li>Throughput: - It is the num per unit time. It is a measure CPU is busy in executing protothe system. Throughput deperfor any process.</li> </ul>	riteria. rogramming the main objective e. CPU utilization can range fro ber of processes that are comple of work done in the system. We pocesses, then work is being don ends on the execution time requ	4M Any four scheduling criteria -1M each eted /hen e in ired



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		<ul> <li>Turnaround time: -The time interval from the time submission of a process to the time of completion of that process is called as turnaround time. It is the sum of time period sp waiting to get into the memory, waiting in the ready que executing with the CPU, and doing I/O operations.</li> <li>Waiting time: - It is the sum of time periods spent in the ready queue by a process. When a process is selected from job pool, it loaded into the main memory. A process waits in ready queue CPU is allocated to it.</li> </ul>	of cess pent cue, ady it is till	
3.	a)	Attempt any <u>THREE</u> of the following: Draw and explain process state diagram.		12 4M
	Ans.	Different process states are as follows: 1. New 2. Ready 3. Running 4. Waiting 5. Terminated	E	Process state diagram 2M Explanation 2M
		new admitted interrupt exit terminated ready running I/O or event completion scheduler dispatch waiting		
		<b>New:</b> When a process enters into the system, it is in new state. In the state a process is created. In new state the process is in job pool.	this	
		<b>Ready:</b> When the process is loaded into the main memory, it is reafor execution. In this state the process is waiting for process allocation.	ady ssor	



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	<b>Running:</b> When CPU is available, system main memory and executes all the instru- when a process is in execution, it is in system, only one process can be in the system, there can be multiple processes state.	tem selects one process f uctions from that process. running state. In single e running state. In multi- es which are in the runn	rom So, user user ning	
	Waiting State: When a process is in exercise resources. If the resource is not avail waiting state. When the resource is avail ready state.	ecution, it may request for lable, process goes into able, the process goes bac	I/O the k to	
	<b>Terminated State:</b> When the process completes its execution state. In this state the memory occupied	on, it goes into the termine by the process is released.	ated	
<b>b</b> )	Describe conditions for deadlock prev	ention.	4	M
Ans.	By ensuring that at least one of below co	onditions cannot hold, we	can	
	<ul> <li>prevent the occurrence of a deadlock.</li> <li><b>1.Mutual Exclusion:</b> The mutual-exclusion condition co</li></ul>	ust hold for non-shar require mutually exclu dlock.	Any cond 1M able sive	y four ditions ' each
	2.Hold and Wait: One way to avoid this Hold and Wait resource; it does not hold any other resource •One protocol that can be used requires be allocated all its resources before it be •Another protocol that can be used is, resources only when the process has resources has resources and use them. Before resources, it must release all the resource to it.	is when a process reques urces. s each process to request gins execution. to allow a process to req none. A process may req e it requests any addition res that are currently allocation	ts a and uest uest onal ated	
	<b>3.No Preemption:</b> If a process that is holding some resour that cannot be immediately allocated to being held are preempted. That is th	ces requests another resolution it, then all resources curre nese resources are implicit	urce ntly citly	



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	released. The preempted resource for which the process is waiting. all the resources i.e. its old resource requesting will be available. <b>4.Circular Wait</b> Circular-wait condition never hold resource types, and to require tha an increasing order of enumeratio Let $R = \{R1, R2,, Rn\}$ be the each resource type a unique int compare two resources and to another in our ordering. Formally _ N, where N is the set of natural	es are added to the list of resou Process will be restarted only w ces, as well as the new ones that ds is to impose a total ordering o at each process requests resource n. set of resource types. We assig teger number, which allows us determine whether one preco y, define a one-to-one function l numbers.	rces hen it is f all s in n to s to edes F: R	
c) Ans.	<ul> <li>Explain fixed size memory partification of the second se</li></ul>	itioning. g (Static) per of fixed size partitions, which ry partitioning. y one process. be executed depends on number elected process from the input quan. the partition becomes available a table indicating parts of men are occupied. ilable for user processes and it of available memory, a hole. enough hole of memory is alloc.	ch is Ca Expl r of leue e for hory it is ated	<b>1M</b> prrect anation 4M
d)	Explain linked file allocation me	ethod.		4M
	<ul> <li>This allocation is on the basis contains a pointer to the next b</li> <li>The disk block can be scattered</li> </ul>	s of an individual block. Each bi block in the chain. ed anywhere on the disk.	lock Co expl	orrect anation 4M,



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Sub	ject: Oper	<ul> <li>The directory contains a pointer to the file.</li> <li>To create a new file, simply create a</li> <li>The following figure shows the link</li> </ul>	Subject Code: the first and the last block a new entry in the directory red allocation. Directory file start end jeep 9 25	22516 as of Diagram Optional
		<ul> <li>20 21 22 23 24 25 26 27 28 29 30 31</li> <li>There is no external fragmentation at a time.</li> <li>The size of a file need not be declar</li> <li>A file can continue to grow as long</li> <li>This method is used only for a sequ</li> <li>This method requires more space to</li> <li>So instead of blocks, clusters are creates internal fragmentation.</li> </ul>	since only one block is need ed when it is created. as free blocks are available ential access files store pointers a used for allocation but	eded this
4.	a)	Attempt any <u>THREE</u> of the following Compare between command line ar (Any four points)	g: nd Graphical user interf	face. 12 4M



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Ans.	Parameter	Command Line Interface(CLI)	Graphic User Interface(GUI)	Any four points 1M each
	Definition	Interaction is by typing commands	Interaction with devices is by graphics and visual components and icons	
	Understan ding	Commands need to be memorized	Visual indicators and icons are easy to understand	
	Memory	Less memory is required for storage	More memory is required as visual components are involved.	
	Working Speed	Use of keyboard for commands makes CLI quicker.	Use of mouse for interaction makes it slow	
	Resources used	Only keyboard	Mouse and keyboard both can be used	
	Accuracy	High	Comparatively low	
	Flexibility	Command line interface does not change, remains same over time	Structure and design can change with updates	
b) Ans.	Write any for System calls 1 1. create 2. delete 3. open f 4. close f 5. create 6. delete 7. read, w 8. getfile 9. set file	ur systems call related related to file manager new file existing file ile ile directories directories vrite, reposition in file attributes e attributes	to file management. nent are:	4M Any 4 system calls IM each



Subj	ject: Oper	rating S	lystem	Subject Code:	22	516	
	c)	Comp four p	are between Long term and points)	d short term scheduler. (	Any	4N	1
	Ans.	Sr. No	Long Term Scheduler	Short Term Scheduler		Any f points eac	four s 1M sh
		1	It is job scheduler	It is CPU scheduler			
		2	It selects processes from job pool and loads them into memory for execution	It selects processes from ready queue which are read to execute and allocate CPU to one of them	n y es		
		3	Access job pool and ready queue	Access ready queue an CPU	d		
		4	It executes much less frequently. It executes when memory has space to accommodate new process.	It executes frequently. executes when CPU available for allocation	It is		
		5	Speed is less than short term scheduler	Speed is fast			
		6	It controls the degree of multiprogramming	It provides lesser control over degree of multiprogramming			
		7	It chooses a good process that is a mix-up of input/output bound and CPU bound.	It chooses a new process fo a processor quite frequently	r 7.		
	d)	Solve algori for ea	given problem by using thm using Gantt chart. Calc ch algorithm	SJF and FCFS schedu ulate the average waiting t	ling ime	4N	1



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Subj	ject: Oper	rating Syste	em	Subject Code:	22516	
		Pro	ocess	Burst time (in ms)		
			P1	9		
			P2	7	-	
			P3	3		
		- Cotos	P4	7		
	Ans.	Gantt Ch	art SJF			
		P3	P2	P4	P1	For each
		0	3	10	17	26 scheduling Gantt chart
		Waiting 7	<i>1M</i> ,			
		P1=17 P2=3				Each
		P3=0				average
		P4=10	• • • • • •	<b>TTT</b>	11 / NT 1	calculation
		Average	waiting tin	ne=waiting time of	all processes / Num	per of 1M
		Processes		=(17+3+0+10)	/4	
				=30/4	- / \	
				=7.5 millise	econds (ms)	
		Gantt Ch				
				- Da	D2 D4	1
		0	P1	9 16	ro r4 19 26	6
				2 10	17 20	,
		Waiting 7	Гіте			
		P1=0				
		P2=9				
		P4=19				
		Average	waiting tin	ne=Waiting time of	all processes / Numb	ber of
		processes		(0.0.17.10)	14	
				=(0+9+16+19) -44/4	/4	
			=11 milli	seconds (ms)		



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e) Ans.	<ul> <li>e) Ans. Describe free space management technique. (Any two) A file system is responsible to allocate the free blocks to the file therefore it has to keep track of all the free blocks present in the disk. There are mainly four approaches by using which, the free blocks in the disk are managed.</li> <li>1. Bit Vector</li> <li>2. Linked List</li> <li>1)Bit Vector: The free-space list is implemented as a bit map or bit vector. Each block is represented by 1 bit. If the block is free, the bit is 1; if the block is allocated, the bit is 0. For example, consider a disk where blocks 2, 3, 4, 5, 8, 9, 10, 11, 12, 13 are free and the rest of the blocks are allocated. The free-space bit map would be : 0011110011111100</li> </ul>										4M Any 2 techniques Correct Explanation 2M each						
	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	
	0	0	1	1	1	1	0	0	1	1	1	1	1	1	0	0	
	0       0       1																



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		$ \begin{array}{c}     frez-space \\     list head   \end{array}                                  $	
5.	9)	Attempt any <u>TWO</u> of the following: Write two uses of following O.S. tools	12 6M
	a)	(i) Device Management	UIVI
		(ii) Performance monitor	2 uses of
		(iii) Task Scheduler	each tool
	Ans.	i) Device management:	2M
		• Managing all the hardware or virtual devices of computer system.	
		• Allow interaction with hardware devices through device driver.	
		• Used to install device and component-level drivers as well as	
		associated software.	
		• Allocate devices to the process as per process requirement and priority	
		<ul> <li>Deallocate devices either temporarily or permanently depending</li> </ul>	
		on condition.	
		• Keeping track of all device's data and location.	
		• Monitoring device status like printers, storage drivers and other	
		devices.	
		• Used to enforce the predetermined policies and decides which process receives the device when and for how long	
		ii) Performance monitor	
		1. Monitor various activities on a computer such as CPU or memory	
		usage.	
		2. Used to examine how programs running on their computer affect computer's performance	
		3. It is used to identify performance problems or bottleneck that	
		affect operating system or installed applications.	
		4. Used to observe the effect of system configuration changes.	



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	iii) Task scheduler																					
	1. Assign processor to task ready for execution																					
	2. Executing predefined actions automatically whenever a certain set																					
	of condition is met.																					
 • `	(Any two relevant uses shall be considered)																					
b)	Writer the outputs of following commands												6M									
	(i) Wait 2385018 (ii) Shoen 40																					
	(II) Sleep U9 (iii) DS - n Asha																					
Ans	(III) PS -u Asha i) Wait command waits until the termination of specified process ID																					
<b>AII</b> 5.	1) wait command waits until the termination of specified process ID 2385018												2M for each									
	ii) Slee	ep c	com	ma	nd	is u	ised	l to	de	lav	for	9 s	eco	ond	s dı	ırir	ng t	he	exe	cut	ion	correct
	of a	pro	oce	ssi.	e. it	t wi	ill p	au	se tl	he 1	tern	nina	al f	or 9	) se	cor	nds.					output
	iii) ps	co	mn	nano	d v	vith	u -u	is	us	ed	to	dis	pla	y c	lata	/pr	oce	esse	s f	or	the	
	specifi	c u	ser	Asl	na.																	
<b>c</b> )	Given	a	pa	ge	re	fer	enc	e	stri	ng	W	ith	th	ree	e ((	)3)	pa	age	fr	am	ies.	6M
	Calcu	late	e tl	he	pa	ge	fa	ult	s v	wit	h '	O	otin	nal	' a	Ind	[ 6]	LR	U'	p	age	
	replac	em	ent	alg	gori	ith	m r	esp	pect	ive	ely.											
	•7,0,1,	2,0,	,3,0	,4,2	2,3,	0,3	,2,1	,2,	0,1	,7,0	),1			1.								
Ans.	(Repr	esei	ntai	tior	1 01	fra	amo	e ca	an I	be i	n a	ny	ore	der	)							Calculate
	I) Opt	iiiia	11																			page fault with
	Ref	7	0	1	2	0	3	0	4	2	3	0	3	2	1	2	0	1	7	0	1	
	F1	7	7	7	n															0	1	relevant
						2	2	2	2	2	2	2	2	2	2	2	2	2	7	7	1	relevant diagram- 2M arak
	F2	/	/	/	2	2	2	2	2	2	2	2	2	2	2	2	2	2	7	0 7 0	1 7 0	relevant diagram- 3M each
	F2	/	0	/ 0	2 0 1	2 0	2 0 3	203	2 4 3	2 4 3	2 4 3	2 0 3	2 0 3	2 0 3	2 0	2 0	2 0 1	2 0	7 0 1	0 7 0	1 7 0	relevant diagram- 3M each
	F2 F3		7 0	/ 0 1	2 0 1	2 0 1	2 0 3	2 0 3	2 4 3	2 4 3	2 4 3	2 0 3	2 0 3	2 0 3	2 0 1	2 0 1	2 0 1	2 0 1	7 0 1	0 7 1	1 7 0 1	relevant diagram- 3M each
	F2 F3 Fault	7 F	7 0 F	7 0 1 F	2 0 1 F	2 0 1	2 0 3 F	2 0 3	2 4 3 F	2 4 3	2 4 3	2 0 3 F	2 0 3	2 0 3	2 0 1 F	2 0 1	2 0 1	2 0 1	7 0 1 F	0 7 1	1 7 0 1	relevant diagram- 3M each
	F2 F3 Fault Total p	7 F Dage	7 0 F e fa	7 0 1 F ults	2 0 1 F 5-9	2 0 1	2 0 3 F	2 0 3	2 4 3 F	2 4 3	2 4 3	2 0 3 F	2 0 3	2 0 3	2 0 1 F	2 0 1	2 0 1	2 0 1	7 0 1 F	7 0 1	1 7 0 1	relevant diagram- 3M each
	F2 F3 Fault Total p	F Dage	7 0 F e fa	7 0 1 F ults	2 0 1 F 5- 9	2 0 1	2 0 3 F	2 0 3	2 4 3 F	2 4 3	2 4 3	2 0 3 F	2 0 3	2 0 3	2 0 1 F	2 0 1	2 0 1	2 0 1	7 0 1 F	7 0 1	1 7 0 1	relevant diagram- 3M each
	F2 F3 Fault Total p <b>ii) LR</b>	7 F Dage	7 0 F e fa	/ 0 1 F ults	2 0 1 F 5-9	2 0 1	2 0 3 F	2 0 3	2 4 3 F	2 4 3	2 4 3	2 0 3 F	2 0 3	2 0 3	2 0 1 F	2 0 1 2	2 0 1	2 0 1	7 0 1 F	7 0 1	1 7 0 1	relevant diagram- 3M each
	F2 F3 Fault Total p <b>ii) LR</b> Ref F1	7 F Dage	7 0 F e fa 0 7	7 0 1 F ults 1 7	2 0 1 F 5-9 2 2	2 0 1 0 2	2 0 3 F 3 2	2 0 3 0 2	2 4 3 F 4 4	2 4 3 2 4	2 4 3 3 4	2 0 3 F 0	2 0 3 3 3 0	2 0 3 2 0	2 0 1 F 1 1	2 0 1 2 1	2 0 1 0	2 0 1 1	7 0 1 F 7	7 0 1 0	1 7 0 1	relevant diagram- 3M each
	F2 F3 Fault Total p ii) LR Ref F1	7 F Dage U 7 7	7 0 F e fa 0 7	7 0 1 F ults 1 7	2 0 1 F 5-9 2 2	2 0 1 0 2	2 0 3 F 3 2	2 0 3 0 2	2 4 3 F 4 4	2 4 3 2 4	2 4 3 3 4	2 0 3 F 0 0	2 0 3 3 0	2 0 3 2 0	2 0 1 F 1 1	2 0 1 2 1	2 0 1 0 1	2 0 1 1 1	7 0 1 F 7 1	0 7 0 1 1 0 1	1 7 0 1	relevant diagram- 3M each
	F2 F3 Fault Total p <b>ii) LR</b> Ref F1 F2	7 F Dage U 7 7	7 0 F e fa 0 7 0	7 0 1 F ults 1 7 0	2 0 1 F 5-9 2 2 0	2 0 1 0 2 0	2 0 3 F 3 2 0	2 0 3 0 2 0	2 4 3 F 4 4 0	2 4 3 2 4 0	2 4 3 3 4 3	2 0 3 F 0 0 3	2 0 3 3 0 3	2 0 3 2 0 3	2 0 1 F 1 1 3	2 0 1 2 1 3	2 0 1 0 1 0	2 0 1 1 1 1 0	7 0 1 F 7 1 0	7 0 1 0 1 0	1 7 0 1 1 1 1 0	relevant diagram- 3M each
	F2 F3 Fault Total p <b>ii) LR</b> Ref F1 F2 F3	7 F Dage U 7 7	7 0 F e fa 0 7 0	7 0 1 F ults 1 7 0 1	2 0 1 F S-9 2 2 0 1	2 0 1 0 2 0 1	2 0 3 F 3 2 0 3 2	2 0 3 0 2 0 3	2 4 3 F 4 4 4 0 3	2 4 3 2 4 0 2 2	2 4 3 3 4 3 2 5	2 0 3 F 0 0 0 3 2 2	2 0 3 3 0 3 2	2 0 3 2 0 3 2	2 0 1 F 1 1 3 2	2 0 1 2 1 3 2	2 0 1 0 1 0 2	2 0 1 1 1 1 0 2	7 0 1 F 7 1 0 7	7 0 1 0 1 1 0 7	1 7 0 1 1 1 1 1 0 7	relevant diagram- 3M each
	F2 F3 Fault Total p <b>ii) LR</b> Ref F1 F2 F3 Fault	7 F Dage U 7 7 F	7 0 F e fa 0 7 0 F	7 0 1 F ults 1 7 0 1 F	2 0 1 F 5-9 2 2 2 0 1 F	2 0 1 0 2 0 1	2 0 3 F 3 2 0 3 F	2 0 3 0 2 0 3	2 4 3 F 4 4 4 0 3 F	2 4 3 2 4 0 2 F	2 4 3 4 3 4 5 F	2 0 3 F 0 0 0 3 2 F	2 0 3 3 0 3 2	2 0 3 2 0 3 2	2 0 1 F 1 1 3 2 F	2 0 1 2 1 3 2	2 0 1 0 1 0 2 F	2 0 1 1 1 1 2	7 0 1 F 7 1 0 7 F	7       0       1       0       1       0       7       0       7	1 7 0 1 1 1 1 1 0 7	relevant diagram- 3M each



MAHARASHTRA STATE BOARD OF TECHNICAL EDUCATION

(Autonomous) (ISO/IEC - 27001 - 2005 Certified)





(ISO/IEC - 27001 - 2005 Certified)

Subj	ect: Oper	rating System		Subject Code:	22	2516				
	b)	Consider the following memory map and assume a new process P4 comes with memory requirements of 6 KB. Locate (Draw) this process in memory using.								
		i) First fit ii) Best Fit iii) Worst Fit	O.S. P1 <free> 12 KB P2 <free> 19 KB P3 <free> 7KB Memory</free></free></free>			diagran	n 2M			
	Ans.	First Fit: Allocate the first free block to the new process P4. O. S. P1 P4 6KB <free> 6KB P2 <free> 19 KB P3 <free> 7 KB</free></free></free>	Best Fit: Alloc the smallest f block that is enough accommodate r process P4. O. S. P1 <free> 12 KI P2 <free> 19 KI P3 P4 6 KB <free> 1 KF</free></free></free>	eate Worst fit: Allocative the largest fit block to the new process P4.          O. S.         P1         3         P1         3         P2         3         P4 6 KB         P3 <free> 7 KB</free>						
	c)	Construct and explain terms of two level and	n directory structu l tree structure.	re of a file system in		6N	ſ			
	Ans.	Construct and explain directory structure of a file system in terms of two level and tree structure. 1) Two-level directory: - In the two-level structures, each user has its own user file directory (UFD). The UFD lists only files of a single user. System contains a master file directory (MFD) which is indexed by user name or account number. Each entry in MFD points to the UFD for that user. When a user refers to a particular file, only his own UFD is searched. Different users can have files with the same name, as long as all the file names within each UED are unique								



## WINTER – 2022 EXAMINATION MODEL ANSWER

#### Subject: Operating System



